SYSTEM AND METHOD OF CALIBRATING A READ CIRCUIT IN A MAGNETIC MEMORY

Abstract of the Disclosure

A system and method of calibrating a read circuit in a magnetic memory is disclosed. In one embodiment, the method includes measuring a calibration value. A large error calibration is performed if the calibration value is within a maximum range. A small error calibration is performed if the calibration value is within a minimum range. The method may include performing a first read operation on the magnetic memory, and performing a second read operation on the magnetic memory.